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# ABSTRACT

To reason about and enforce security in dynamic software systems, automated analysis and verification approaches are required. However, such approaches often encounter scalability issues, particularly when employed for runtime analysis, which is necessary in software systems with dynamically changing architectures, such as self-adaptive systems. In this work, we propose an automated formal approach for security analysis of component-based systems with dynamic architectures. This approach leverages formal abstraction and incremental analysis techniques to reduce the complexity of runtime analysis. We have implemented and evaluated our approach against ZNN, a widely known self-adaptive system exemplar. Our experimental results demonstrate the effectiveness of our approach in addressing scalability issues.

## CCS CONCEPTS

• Security and privacy  $\rightarrow$  Formal security models; Software security engineering; • Software and its engineering  $\rightarrow$  Model checking; • Computer systems organization  $\rightarrow$  Self-organizing autonomic computing.

#### **KEYWORDS**

Security Analysis, Model Checking, Runtime Security

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#### **1 INTRODUCTION**

Today's software landscape moves towards heterogeneous and dynamic systems to cope with a frequently changing environment and user requirements. Due to the adapting nature and various sources of uncertainty in such systems, new



This work is licensed under a Creative Commons Attribution International 4.0 License. ASE '24, October 27-November 1, 2024, Sacramento, CA, USA © 2024 Copyright held by the owner/author(s). ACM ISBN 979-8-4007-1248-7/24/10. https://doi.org/10.1145/3691620.3695499 security threats that are unknown at design time can arise. Such threats can lead to exposing a larger attack surface throughout a system's lifetime compared to systems with a static architecture and might introduce new opportunities for vulnerability exploitation. Further, cyber-attacks grow in scale, sophistication and frequency, as new vulnerabilities are introduced and novel exploitation techniques and strategies are discovered [2, 17, 45]. Conventional offline security analysis approaches might be of limited effectiveness, as they may fail to consider security threats introduced as the system's behavior and attack surface evolve at runtime.

Key Challenges. Changes in self-adaptive systems can happen at the architectural level or in the components' behavior. In systems where the system architecture and its vulnerabilities are dynamic and unknown a priori at design-time, *automated* security analysis and enforcement approaches are required to continuously analyse security at runtime and react to security-affecting changes to protect the system (C1). Online methods to analyze security [5, 6, 34, 54] often rely on runtime monitoring [5, 6, 54], where an observer monitors the executions of a software system to verify if its behavior adheres to a set of formal specifications [39]. These approaches only check the current execution to detect security violations which makes the analysis incomplete and thus are unsuitable for security threat analysis, since not all possible execution paths are verified (C2). Skandylas et al. [53] proposed a formal approach using model checking for runtime security analysis of component-based self-adaptive systems that considers the probabilistic attacker behaviour and runtime architectural changes but it suffers from scalability issues. Abstraction is a technique to reduce the analysis complexity by abstracting away details. One way to abstract away details is to perform the analysis at the architectural level where the internal behaviour of the components is omitted [32]. Even though architecture-level security analysis can reduce complexity, the scalability of formal techniques such as model checking is limited when analysing component-based systems due to the state space explosion problem [42] (C3). Modular verification is a common method to tackle complexity that aims to reduce the verification problem into verifying smaller subproblems. Modular analysis approaches have been used to analyse component-based systems in the past, e.g., incremental [9, 32, 38], on-the-fly [25, 51] and compositional verification [10], and assume-guarantee reasoning [46].

To address challenges C1-C3, there is a need for automated security analysis approaches that (i) consider uncertainties in the attacker behaviour, (ii) use modular analysis to scale up the reasoning to realistic systems, and (iii) perform the security analysis at runtime to cope with architectural changes or newly discovered avenues of attack.

The method proposed in [53] designs self-protecting systems using runtime threat analysis and addresses C1-C2, however, it suffers from scalability issues. To capture the uncertainty involved in the attacker behaviour, it employs a *probabilistic* modelling approach (C1), and given the system architecture and its vulnerabilities, it automatically builds and analyzes the security model(C2). This paper builds upon [53] and proposes a new *automated* approach for security threat analysis of component-based self-adaptive systems that utilizes a combination of techniques to tackle complexity, including *formal abstraction*, and *incremental security model construction and verification*.

Contributions. We propose a two-layer probabilistic security model. The high-level layer is an abstraction over the lower-level layer (See Figure 2). The lower layer formalizes a probabilistic attack scenario and the attacker's interactions with the component-based system. We propose a sound algorithm to build the abstract layer that automatically partitions the low-level layer into *fragments*, verifies probabilistic properties on them, and uses the results to build the probabilistic abstract layer. Verification is then performed on the high-level layer, to reduce the analysis complexity and improve scalability. We use the PRISM [29] model checker for verification, and PCTL (Probabilistic Computation Temporal Logic) [28] to specify properties. Our approach is sound, in the sense that model checking a subset of PCTL formulas against the high-level model and the low-level model leads to identical results.

To improve the scalability of our approach for runtime analysis, we employ incremental model update and verification. When a component-based system changes at runtime, often only a subset of its components are altered. We identify the components and fragments affected by a change, reconstruct and reverify the modified fragments of the low-level model, and update the corresponding parts of the high-level layer automatically. We have automated the whole approach and conducted experiments to study the effects of modularization and incremental update on the analysis complexity. The contributions of this paper include:

- [Scalability, C3] We exploit formal abstraction and modular analysis to automatically build a two-level formal security model. This model enables us to probabilistically reason about the security threats at an abstract level, either statically or at runtime, to tackle the analysis complexity. We employ a *probabilistic* approach to model and reason about uncertainty.
- [Automation, C1] The security model is automatically built from the system architecture described in an Architecture Description Language (ADL) [24] and the formal specification of its vulnerabilities. The security

model is incrementally updated at runtime to reflect the changes, e.g., when new architectural changes or new avenues of attack are discovered.

- [Soundness, C2] We use model checking at runtime to analyze threats against the system. and formally prove that our approach is sound.
- [*Experiments*] We implement and apply our approach on a case study, and conduct experiments to study its performance. The results show that it reduces the complexity of the security analysis and offers an improvement in scalability.

An earlier version of this work, with detailed examples, algorithms, and an additional case evaluation is available at [52].

The rest of the paper is structured as follows. Section 2 presents a running example that we use for demonstrative purposes throughout this paper. Section 3 discusses the required preliminaries. Section 4 formally introduces our two-level security model. Section 5 details the modular generation, incremental update and verification of our model. Section 6 provides a proof of soundness for our abstraction. Section 7 discusses the evaluation of our approach. Section 8 discusses related work, and Section 9 concludes the paper.

#### 2 RUNNING EXAMPLE

A component-based system is composed of a set of components where a component provides interfaces to be invoked by other components. Interfaces might contain security vulnerabilities, i.e., weaknesses that can be exploited to attack the system. Figure 1 shows an example system called Insecure-Store that we use in this paper as a running example. The user can add, remove and lookup documents in this document handling system. InsecureStore includes five initial components: Frontend, Backend, UserMgr, FileMgr, and Database.

InsecureStore is capable of altering its architecture at runtime to better meet its goals. An example architectural change is shown in Figure 1(b) where a FileServer component is added to the system to improve its efficiency when dealing with a large number of document retrieval queries. Instead of retrieving documents from the Database component, the FileMgr component now requests a document from the newly added file server. The security characteristics of these two architectures are different. An attack exploiting sql\_injection that was available in the architecture of Figure 1(a) by invoking the exec\_query interface of the Database from the FileMgr component is no longer possible. However, a new path of exploitation has been uncovered in Figure 1(b), i.e., the get\_document interface of the FileServer is callable and vulnerable to path injection.

## **3 PRELIMINARIES**

This section introduces probabilistic model checking and attack modeling for component-based systems.

#### 3.1 Probabilistic model checking

The goal of verification is to check whether a system model  $\mathcal{T}$  satisfies a specification  $\psi$ . Probabilistic model checking

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(b) InsecureStore architecture after adaptation Figure 1: Two possible InsecureStore architectures

is a technique to verify quantitative properties of a system that exhibits stochastic behavior [36]. We specify the system behaviour using *Probabilistic Transition Systems* (PTS).

**Definition 1** (PTS). A probabilistic transition system is a tuple  $\mathcal{T} = (V, V_0, \mathbb{T})$  where V is a set variables,  $V_0$  shows the initialization of the variables, and  $\mathbb{T}$  is a set of probabilistic transitions. A transition is a tuple  $(\phi, \mathbb{D}_U)$  where  $\phi$  is the transition guard, a Boolean expression over V, and  $\mathbb{D}_U$  is a discrete probability distribution function over the set of updates U.

Initially,  $\mathcal{T}$  is in its initial state. A transition is fired if its guard is satisfied, and when fired the variables are updated according to its update functions. The semantics of PTS is defined as a *Discrete-Time Markov Chain* (DTMC).

**Definition 2** (DTMC). A DTMC is defined as a tuple  $\mathcal{D} = (S, V, \Sigma, s_0, \mathbf{P}, \mathcal{L})$ . S is the set of states defined over the set of variables V.  $\Sigma$  is a set of actions including the internal action  $\tau$ .  $s_0 \in S$  is the initial state.  $\mathbf{P} : S \times S \rightarrow [0, 1]$  is the transitions probability matrix where  $\sum_{s' \in S} \mathbf{P}(s, s') = 1$  for all  $s \in S$ , and  $\mathcal{L}$  is a labelling function that assigns to each state a set of atomic propositions over V that hold in that state.

PCTL [28] is a logic for probabilistic reasoning about temporal properties of a system with stochastic behaviour, for example: "Is the probability that the attacker compromises the network less than 0.5?"? This logic contains state formulas  $\phi$  and path formulas  $\psi$ , the syntax of which is given by the following grammar:

where  $\top$  denotes "true",  $\alpha$  is a proposition,  $\bowtie \in \{<,\leq,>,\geq\}$ , and  $p \in [0, 1]$ . The operators X ("next"), U ("until") and F ("eventually") are the standard temporal operators. Informally,  $\mathsf{P}_{\bowtie p}[\psi]$  states that the path formula  $\psi$  will be satisfied with a probability meeting the bound  $\bowtie p$ .  $\mathsf{P}_{=?}[\psi]$  denotes computing the probability of satisfying the property  $\psi$ . We write  $s \vDash \psi$  to indicate that state s of  $\mathcal{D}$  satisfies the formula  $\psi$ , and write  $\mathcal{D} \vDash \psi$  if  $s_0 \vDash \psi$ . A property is satisfied by a PTS, if and only if it is satisfied by its semantics described as a DTMC.

#### 3.2 Component-based System Attack Modeling

We define a software architecture as as a collection of components and interactions between them. Each component satisfies parts of the system's functionality which is exposed to the rest of the system via a number of interfaces that can be invoked to facilitate communication between the components.

A security-aware architecture [53] is a software architecture enhanced with security-related information including the vulnerabilities associated with each component, the interfaces that can be invoked to exploit those vulnerabilities and how vulnerabilities can be combined to form more complex attacks that allow the attacker to compromise the system further. Figure 1 depicts two possible security-aware architectures of InsecureStore. A security-aware architecture  $A = \langle C, R \rangle$ is a pair where a component  $c \in C$  is defined as  $(I, \mathcal{V}), I$  is the set of c's interfaces used for communication with other components, and  $\mathcal{V}: I \to 2^V$  is a function mapping each interface  $i \in I$  to a possibly empty set of vulnerabilities in V, and V is the set of all vulnerabilities. Further, R is a set of component interactions. An interaction r is a triple (c', i, c)where  $c, c' \in C$  are interacting components and  $i \in I$  is an interface provided by c. This means that the component c'invokes the interface i of the component c.

We rely on an architectural model of the system that is maintained at runtime to support incremental updating of our security model. The architectural model contains the concrete security-aware architecture, component types, vulnerabilities, attack rules to chain vulnerabilities to create attacks and their effects. Vulnerabilities, and the rules for complex attacks and security impact are modeled as opaque architectural properties, and include the required attack probabilities. When the system architecture changes, the corresponding changes are reflected to the architectural model of the system automatically.

A logical attack graph is used to illustrate and analyze the exploitation of security weaknesses and vulnerabilities to compromise a system. Figure 4(a) shows an example attack graph. A logical attack graph shows different strategies to reach a *final goal*, and comprises three types of nodes: *primitive nodes* (rectangles) that state the initial known facts about the attack, *derived nodes* (diamonds) which correspond to derived knowledge, and *rule nodes* (ellipses) that are rules used to derive new derived nodes via resolution [43]. When the preconditions of a rule node hold (i.e., its predecessors),

the rule is enabled and the attacker can gain the rule's consequence (i.e., its successor), which is a derived node. To generate the logical attack graph, we use a set of rules, contained in the architectural model, to specify vulnerability exploitation, the security-aware architecture and the attack goal, which are then fed to a tool called MulVAL [44] to generate a logical attack graph.

## 4 SECURITY MODEL

#### 4.1 Scope and Overview

Scope. Our approach targets component-based systems with dynamic software architectures and can be used by designers and operators of such systems to analyze their security. This approach can be deployed alongside the system at runtime to continuously monitor and evaluate its security. Although in this paper we primarily focus on self-adaptive component-based systems, our approach is also applicable to other types of software systems, the architectures of which can be modeled as a graph of components and connectors [18], whose vulnerabilities can be identified. Examples of such architectures include microservice and container-based systems. We consider threats that are modeled as exploitable vulnerabilities in the components' interfaces. New threats to the system can arise from architectural changes that introduce or remove attack paths through component invocations or from discovering new vulnerabilities. Further, our approach offers a viable method for analyzing security in systems with known vulnerabilities that have not been remediated. Note that recent research results [48, 57] and industry security reports [12, 56] show that systems are often deployed with known vulnerabilities for various reasons.

We employ probabilities to capture uncertainties in the attacker and system behavior. Sources of uncertainty include, among others: (i) the attacker's skill level and budget to carry out attacks, (ii) the attacker's initial knowledge of the system and exploitation capabilities, (iii) the complexity of the exploitation, (iv) the effectiveness of defense mechanisms in detecting or preventing attacks, and (v) interactions between exploits or system behaviors that depend on the runtime state, e.g., they are timing/memory order dependent.

Security Model Overview. We employ a two-tier model shown in Figure 2 to describe and analyse the security of a system. The bottom layer, called the *low-level layer*, describes the attacker behaviour and her interactions with the system components, i.e., actions by the attacker or by the system that contribute to attacks and the consequences of those actions on the system or attacker behaviour. The top layer is the *highlevel* layer that abstracts away the details of the low-level layer. This layer is constructed based on the verification results of the low-level layer that describe under which conditions security violations can occur and the consequences of such violations. Security properties are then verified against the high-level abstract model, instead of the large low-level model, to tackle analysis complexity. We construct an initial model compositionally, and incrementally update it at runtime to



Figure 2: The Two-Level Security Model where  $\mathcal{M}_i$  is a fragment model,  $\delta$  is a deadlock state showing that the attacker gave up on the attack

reflect changes in its architecture and exploitability, allowing us to modularly reason about security as the system evolves.

#### 4.2 Low-level Layer

An attack scenario describes different strategies an attacker may follow to reach a specific goal. We decompose an attack scenario into four attack phases: discovery, exploitation, *impact* and *system security*. Each attack phase is further decomposed into a number of attack fragments. An attack fragment is a set of actions taken by the attacker to reach a possibly intermediate goal. Figure 3(a) and Figure 3(b) show two attack scenarios where the boxes correspond to attack fragments, the dotted areas correspond to phases, and the arrows between the boxes show the dependencies between attack fragments. In the discovery phase, the attacker identifies the interactions among the system components to gain information about the active components, their exposed interfaces and their interactions. The exploitation phase describes the process by which the attacker interacts with the system under attack. It comprises three steps: (i) vulnerability discovery, i.e., identifying the vulnerable interfaces and available exploits of the active components, (ii) vulnerability exploitation, i.e., exploiting a single vulnerable interface which we call a single-exploit attack, and (iii) combining multiple exploits to perform more complex attacks, which we call *multi-exploit* attacks. The impact phase models the impact of exploitation on each component based on which fundamental security property among confidentiality, integrity and availability is violated. The system security phase, describes the conditions upon which the whole system is rendered non-functional.

We employ a capability-based approach to model the attacker behavior. An attacker capability is the ability to perform a certain set of actions, and is obtained by succeeding in





Oisco

I-Fr conf



I-Backendintegrity

91:

I-FileMgr confidentia

917

I-Backendconfidential

Figure 3: Attack phases and the corresponding attack fragments before and after the adaptation where an element with the prefix 'S-', 'M-', 'I-' respectively shows a single-exploit, multi-exploit and security impact fragment. Goal capabilities, that serve as prerequisites to fragments are prefixed with g.

performing the actions in the associated attack fragment. For example, in order for the attacker to gain unauthorized entry to the system in our running example, she needs a capability to exploit the Backend component via an authentication bypass vulnerability and once successful in bypassing authentication, she can make use of Backend as a staging point to gain further capabilities. When an attacker gains a capability, i.e., she succeeds in achieving a sub-goal, her exploitation capabilities grow. Capabilities show the extent to which the attacker has succeeded in taking over the system and enable her future actions by defining her available exploitation options.

An attack fragment f comprises a set of related activities that are carried out to gain a specific capability called the *fragment goal*. The capabilities are of three types: *prerequisite capabilities* denoted by  $C_P$ , *internal capabilities* denoted by  $C_I$ , and a *goal capability* denoted by  $g_f$ . Internal capabilities are local to the fragment, while a goal capability can be a prerequisite capability of another fragment. Prerequisite capabilities serve as the requirements for gaining internal ASE '24, October 27-November 1, 2024, Sacramento, CA, USA

and/or goal capabilities, while a goal capability is the result of achieving the goal of the attack fragment. An attack fragment might have multiple prerequisite capabilities but will only have a single goal capability. For example, the attack fragment S-Frontend-cwe77 in Fig. 3 is used to gain a capability  $q_5$  that enables the attacker to exploit the connect interface on the Frontend component to perform command injection on it. Command injection in turn allows the attacker to perform a multi exploit attack to achieve authentication bypass on the Backend component, i.e., to gain the capability  $g_9$  in M-Backend-authbypass, in addition to affecting the *confidentiality* of the Frontend component. A fragment name is composed of its fragment type, the affected component and its goal name where the goal name shows either the CWE (Common Weakness Enumeration) [41] identifier where applicable, or the capability gained by the attacker otherwise.

An attack fragment is specified as a PTS that models how the attacker actions affect the security of the system components. We refer to that PTS as a *fragment model*.

**Definition 3** (Fragment model). An attack fragment f is modeled using a PTS  $\mathcal{M} = \langle V, V_0, \mathbb{T} \rangle$  where:

- $V = C_P \cup C_I \cup \{g, \delta\}$  where  $C_P$  and  $C_I$  are sets of Boolean variables used to show the prerequisites and the internal capabilities, g is the fragment goal capability, and  $\delta$  is a boolean variable to indicate whether the attacker quit without achieving the goal g,
- each internal/goal capability c is associated with a probabilistic transition (φ<sub>c</sub>, D<sub>U</sub>) in T where φ<sub>c</sub> is the prerequisites to gain c, and U = {{c := T}, Ø, {δ := T}} is the set of possible updates by this transition. The transition states that when the prerequisites to gain a capability c hold (formula φ<sub>c</sub>), one of the three updates in U is applied with a probability: either (i) the attacker succeeds and c is gained (i.e., the update {c := T}), or (ii) the attacker fails and can retry (i.e., the update Ø), or (iii) the attacker gives up on trying the attack in future (i.e., the update {δ := T}.

Only transitions that lead to the attacker achieving the goal have a probability of giving up greater than zero. That is based on the assumption that the attacker gives up on the goal altogether rather than on performing a specific attack action. The internal actions of a fragment are local to it, i.e., they do not affect the rest of the fragments and are not considered in the high-level model. Thus, the precise internal capability that the attacker failed to gain and gave up is not relevant, as it does not affect the behavior of the high-level model or of any other fragments.

Figure 4(b) shows the transitions of a fragment model. A transition t is written as  $[t] \phi_c \rightarrow p_1 : \{c := \top\} + p_2 : \{\delta := \top\} + p_3 : \emptyset$ , which states when the condition  $\phi_c$  holds, then with probability  $p_1$  the transition a will be taken to set c to  $\top$ , otherwise  $\delta$  will be set to  $\top$  with probability  $p_2$ , or no effect will occur with probability  $p_3$  (i.e., the attack failed). Note that each fragment has a unique goal and the internal capabilities of two attack fragments are disjoint, i.e.,  $g \neq g'$ , and  $C_I \cap C'_I = \emptyset$  where  $f \neq f'$ . However, the goal capability



(b) Fragment model  $(\mathcal{M}_f)$ 



(c) High-level layer  $\mathcal{H}$  (Partial) Figure 4: Transformation of a fragment graph into a fragment model, and its high-level abstraction

of a fragment can be a prerequisite capability of another fragment which establishes a dependency between the two fragments. *Further, we assume the attacker only attempts* to gain a capability if she does not already hold it.

**Definition 4** (Low-level Layer). Let  $F = \{f_0, \ldots, f_n\}$  be the set of attack fragments of an attack scenario. The low-level layer model  $\mathcal{M}$  is defined as the parallel composition of its fragment models, i.e.,  $\mathcal{M}_0 \parallel \cdots \parallel \mathcal{M}_n$  where  $\mathcal{M}_i, 0 \le i \le n$  is the fragment model of  $f_i$ .

## 4.3 High-level Layer

The high-level layer abstracts away the internal behavior of the attack fragments, i.e., the internal capabilities of the attack fragments in the low-level layer, and the model only captures the goal capabilities and prerequisite capabilities.

**Definition 5** (High-Level Model). Let  $\{f_0, \ldots, f_n\}$  be the set of attack fragments in the low-level layer, and  $\mathcal{M}_i = (V_i, V_{0i}, \mathbb{T}_i)$  be the fragment model of  $f_i, 0 \leq i \leq n$  where  $V_i = \mathcal{C}_{P,i} \cup \mathcal{C}_{I,i} \cup \{g_i, \delta_i\}$ . The high-level model is defined as a PTS  $\mathcal{H} = (V, V_0^h, \rightarrow)$  where:

- $V = \bigcup_{0 \le i \le n} V_i \setminus \mathcal{C}_{I,i}$  is the union of the variables of all fragments except for their internal capabilities  $\mathcal{C}_{I,i}$ ,
- each goal capability  $g_i$  belonging to a fragment  $f_i$  is associated with a probabilistic transition  $(\phi_{g_i}, \mathbb{D}_U)$ where  $\phi_{g_i}$  is the prerequisite to gain the fragment goal  $g_i$ , and  $U = \{\{g_i := \mathsf{T}\}, \{\delta_i := \mathsf{T}\}\}$ . This transition states when the prerequisites to gain the capability  $g_i$  hold (formula  $\phi_{g_i}$ ), then either (i) the attacker succeeds to reach the goal  $g_i$ , or (ii) the attacker gives up at some stage of the attack.  $\mathbb{D}_U(\{g_i := \mathsf{T}\})$  is the probability of

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reaching  $g_i$  in its corresponding fragment model  $\mathcal{M}_i$ , and  $\mathbb{D}_U(\{\delta_i := \mathsf{T}\}) = 1 - \mathbb{D}_U(\{g_i := \mathsf{T}\}).$ 

## 5 AUTOMATED SECURITY MODEL CONSTRUCTION AND INCREMENTAL UPDATE

In this section, we discuss how we automatically partition an attack scenario into fragments, modularly construct the model and incrementally update it.

#### 5.1 Automated Modular Model Construction

5.1.1 Low-level Layer Partitioning and Construction. To construct the low-level layer *automatically*, we first generate a logical attack graph [44] to describe each attack fragment, called a *fragment graph*, and then transform this graph into a fragment model  $\mathcal{M} = (V, V_0, \mathbb{T})$  where V is the set of derived nodes and primitive nodes in the fragment graph that represent the attacker capabilities. All primitive capabilities are initialized to true in  $V_0$  while the derived capabilities have the initial values of false. This is due to the fact that the attacker already holds the primitive capabilities, and follows the attack strategy to gain the derived capabilities. According to the semantics of rule nodes, when the attacker holds all the predecessors of a rule node, she can apply the rule and gain its successor derived node. Thus, we translate each rule node r with the successor node c to a transition  $\langle \phi_c, \mathbb{D}_U \rangle$  where  $\phi_c$  is the conjunction of the capabilities associated with r's predecessors, and  $\mathbb{D}_U$  is defined according to Definition 3 where the capability c is the rule successor.

**Example 1.** Figure 4(a) shows a fragment graph, and Figure 4(b) is its corresponding model. The label of a rule node shows its name separated by ":" from its success probability. The goal capability of this fragment is the final node  $g_7$ . For instance, Rule 2 states that when the capabilities  $c_3$  and  $g_4$  (i.e., Rule 2's predecessors) are held by the attacker, and she does not hold the capability  $c_6$  (i.e., Rule 2's successor), she can use this rule and may gain  $c_6$  with probability 0.9 or fail.

The low-level layer generation is carried out modularly using Algorithm 1 which partitions an attack scenario into disjoint fragments and generates their models in parallel automatically. Given an architectural model A, the algorithm first generates a generic attack graph  $\mathcal{G}$  that includes all possible attack patterns (line 2). The attack patterns correspond to chains of rule applications that link a fragment's prerequisite capabilities to its goal capability. An attack pattern is represented by a Horn clause that specifies an attack rule. The rules are parametric and if a rule's parameters can be valuated in a way to match the prerequisites of a fragment, we can call it a matched attack pattern and the instantiated attack pattern constitutes an attack fragment or a part of it.

To perform the instantiation, we employ an exhaustive matching algorithm that will try to match all possible generated goal capabilities of the previous phases with prerequisites of the fragments of the next phase. Then, we use the architectural model A and the generic attack graph  $\mathcal{G}$  to identify the

Algorithm 1: Modular Fragme	nt Model Construction							
Input: Architectural Model A								
Output: Set of fragment models X								
<pre>// The set of all generated fragment graphs</pre>								
1 pFragGraphs = $\emptyset$ , X = $\emptyset$								
// Create the generic attack graph								
$\mathcal{G}$ =CreateGenericAttackGraph(A)								
<pre>// Phase-by-phase fragment generation</pre>	<pre>// Phase-by-phase fragment generation</pre>							
3 for $(depth = 0; depth < maxDepth; d$	3 for $(depth = 0; depth < maxDepth; depth++)$ do							
<pre>// find all depth-th level fragm</pre>	// find all depth-th level fragments given pFragGraphs							
4 F=FindNewFragments(depth,G	$F = FindNewFragments(depth, \mathcal{G}, pFragGraphs)$							
<pre>// generate the fragment attack</pre>	<pre>// generate the fragment attack graphs in parallel</pre>							
5 ParallelGraphGen $(F, \mathcal{G}, pFragGen)$	aphs)							
6 end								
7 for fragGraph $\in$ pFragGraphs do in pa	rallel							
<pre>// build the fragment model from</pre>	the fragment graph							
8 $\mathcal{M}_i = \text{ConstructFragModel}(\text{frag})$	gGraph,A)							
9 $X = X \cup \{\mathcal{M}_i\}$	$ X = X \cup \{\mathcal{M}_i\} $							
10 end	10 end							
11 return X								
12 Function ParallelGraphGen $(F, \mathcal{G}, pFra$	2 Function ParallelGraphGen $(F, \mathcal{G}, pFrags)$ is							
13 for $f \in F$ do in parallel								
<pre>// generate the formal speci</pre>	fication of the fragment							
14 spec = GenerateFormalSpe	$c(f, \mathcal{G})$							
<pre>// generate the fragment att</pre>	ack graph							
15 fragGraph=RunGraphGen(	f, spec)							
16 $pFrags=pFrags \cup \{fragGrap\}$	h}							
17 end								
18 end								

fragments of each phase (line 4) and generate their fragment graphs phase-by-phase (lines 3-6). The fragments of a phase are identified by traversing  $\mathcal{G}$  to find matching fragments F whose prerequisites fragments have been generated, i.e., those fragments that match the attack pattern specified by the attack rules used to generate  $\mathcal{G}$ . If a matching component allocation is found, it means that the attack is possible in the security-aware architecture and hence a fragment must be created for it. The fragment graphs of F are then generated in parallel using the function ParallelGraphGen (lines 13-19). This function automatically generates the specification of each fragment based on the specification of the generic attack patterns specified in first order logic in a similar fashion to [53] (line 15), and then feeds the specification to a logical attack graph generator to generate its graph (line 16). Once all the fragment graphs are built, they are transformed to fragment models in parallel, as discussed earlier (lines 7-10).

5.1.2 Automatic High-Level Model Construction via Formal Abstraction. Algorithm 2 gives the steps to construct a highlevel model from a set of fragment models. The algorithm receives as input the output of Algorithm 1, i.e., a set of fragment models. For each fragment model  $\mathcal{M}_i$ , we first retrieve its associated goal  $g_i$  (line 3), add the goal to the set of variables V (line 4), instantiate said variables (line 5), and model check the property,  $P_{=?}[F g_i]$  to determine the probability that the attacker succeeds in gaining the fragment goal (line 6). Informally, this is equivalent to calculating the probability  $\rho_{g_i}$  that the attacker will eventually succeed in gaining  $g_i$ . We generate a transition for each fragment at the high-level model that either leads to gaining the fragment goal with a probability, or giving up. We add the transition to the set of transitions  $\mathbb{T}$  (line 9). The transition guard is a conjunction of the attacker not having previously succeeded or given up on the goal (i.e.,  $\neg g_i$  and  $\neg \delta_i$ ), and the set of prerequisites that need to hold before the goal can be attempted returned by the function GetPrerequisites. The transition's probability to take the update where the attacker gains the goal  $(g_i = \top)$  is  $\rho_{g_i}$ , while with probability  $1 - \rho_{g_i}$ the attacker gives up  $(\delta_i = \top)$ . The function MakeTransition receives as input the transition guard, the fragment goal  $g_i$ and the probability that the attacker will succeed in gaining the goal  $\rho_{g_i}$  and generates a transition including the  $\delta_i$  update and its corresponding probability.

Algorithm 2: High-Level Model Construction						
Input: A set of fragment models X						
<b>Output:</b> The high-level model $\mathcal{H}$						
1 $\mathbb{T} = \emptyset, V = \emptyset$						
// For each fragment model						
2 for $\mathcal{M}_i \in X$ do in parallel						
<pre>// Retrieve the fragment model's goal</pre>						
$g_i = \text{GetFragmentGoal}(\mathcal{M}_i)$						
$4  V = V \cup \{g_i\} \cup \{\delta_i\}$						
$V_0 = V_0 \cup \{g_i = \bot\} \cup \{\delta_i = \bot\}$						
<pre>// Compute the probability the goal will be met</pre>						
6 $\rho_{g_i} = \text{ModelCheck}(\mathcal{M}_i, \mathcal{P}_{=?}[F \ g_i])$						
// Retrieve fragment graph for $\mathcal{M}_i$						
7 $\operatorname{fragGraph} = \operatorname{getFragmentGraph}(\mathcal{M}_i)$						
<pre>// Retrieve the fragment goal's prerequisites</pre>						
8 $\phi_{g_i} = \neg g_i \land \neg \delta_i \land \text{GetPrerequisites}(g_i, \text{fragGraph})$						
<pre>// Add the goal transition to the high-level model</pre>						
9 $\mathbb{T} = \mathbb{T} \cup \{ \text{MakeTransition}(\phi_{g_i}, g_i, \rho_{g_i}) \}$						
10 end						
11 $\mathcal{H} = (V, V_0, \mathbb{T})$						
12 return $\mathcal{H}$						

**Example 2.** Figure 4 (c) shows the corresponding transition of the fragment shown in Figure 4(b) in the high-level layer. The fragment goal  $g_7$  can be reached via two paths: (i) Rule 1 and Rule 3, or (ii) Rule 2 and Rule 4. The prerequisites for the first path are  $c_1$  and  $g_2$ , and the prerequisites for the second path are  $c_3$  and  $g_4$ .  $g_2$  and  $g_4$  are fragment goals of its predecessors fragments, while  $c_1$  and  $c_2$  are internal capabilities that always hold and can be ignored. Hence, the guard to gain  $g_7$  is holding  $g_2$  or  $g_4$ , and not holding  $g_7$  or giving up. The probability 0.8235 is the model checking result of  $P_{=?}[F \ g_7]$  against the fragment model shown in Figure 4(b).

When an architectural change occurs, a subset of the components and interactions are often altered, added or removed. Thus, a part of the system model and consequently, a part of the attacker behavior will remain unaffected, while the behavior related to the modified part of the system architecture will evolve. Fig. 3(a) shows the state of the low-level layer that evolves to Fig. 3(b) after an adaptation that adds a Filemanager component to the system. There are seven fragments in Fig. 3(a) that belong to the exploitation and impact phases fragment models for Frontend, and Backend. These fragments models have not been altered by the adaptation, rather an extra vulnerable component has been added which has introduced three new fragments (See Fig. 3(b)). Therefore, instead of re-generating the whole low-level model, we update the model by updating the affected fragments that correspond to the components affected by an adaptation. Since the highlevel model is constructed by verifying properties on the

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$[] T \rightarrow p_5 : (q_5=T) + (1-p_5) : (\delta_{q_7}=T)$
$  [] T \rightarrow p_6 : (g_6=T) + (1-p_6) : (\delta_{g_6}=T)$
$[] \ T \rightarrow p_7 : (g_7 = T) + (1 - p_7) : (\delta_{g_7} = T)$
$[] \top \rightarrow p_8 : (g_8=T) + (1-p_8) : (\delta_{g_8}=T)$
$[] g_5 \& g_6 \rightarrow p_9:(g_9=T)+(1-p_9):(\delta_{g_9}=T)$
$[] g_5 \rightarrow p_{10} : (g_{10} = T) + (1 - p_{10}) : (\delta_{g_{10}} = T)$
$[] g_9 \rightarrow p_{11} : (g_{11}=T) + (1-p_{11}) : (\delta g_{11}=T)$
$[] g_7 \rightarrow p_{12} : (g_{12} = T) + (1 - p_{12}) : (\delta_{g_{12}} = T)$
$[] g_8 \to p_{13} : (g_{13} = T) + (1 - p_{13}) : (\delta_{g_{13}} = T)$

(a) Before Adaptation



(b) After Adaptation Figure 5: Before and after adaptation high-level models

low-level model, changes in the low-level model need to be propagated to the high-level model. See [52] for more details about the incremental model update process.

Fig. 5 shows the high-level model for the configurations in Fig. 3. The high-level model is updated with the four new capabilities while the old capabilities associated with the UserMgr component that are no longer present are removed. In Fig. 5(b), additions are highlighted in bold, and removals are shown in strikethrough. In the figure, all variables in the model are initialized to false and the probability associated with a goal capability  $g_i$  is represented by  $p_i$ . For readability, we omitted the negation of give up from the transitions guards. For instance, the last transition in Fig. 5(b) basically represents the following transition: [] not  $\delta_{\mathbf{g}_{17}} \& \mathbf{g}_{16} \to \mathbf{p}_{17} : (\mathbf{g}_{17} = \top) + (\mathbf{1} - \mathbf{p}_{17}) : (\delta_{\mathbf{g}_{17}} = \top)$ .

## 5.2 Verification

We use our security model to perform quantitative security threat analysis via probabilistic model checking. To tackle the analysis complexity, verification of security properties is performed against the high-level model. We express our security properties in  $PCTL_{X}$  which is a subset of PCTL with no next operator. We define an atomic proposition per goal capability or give up variable (See Definition 5). Misusing notation, we represent the atomic proposition that matches a goal capability by the fragment from which the goal capability is derived. When a fragment is prefixed by 'Q' then we refer to the atomic proposition that matches the give up variable of the fragment. For example, the atomic proposition ExploitedFrontendCWE77 represents the attacker gaining  $g_5$ in InsecureStore. We also define systemDown, a Boolean expression over atomic propositions which is defined per system and represents the condition upon which the system can be

considered fully compromised to the extend that it can no longer remain operational. We can express properties to assess different security risks, e.g., (i) to determine the success probability of different attacks to analyse their risks, (ii) the probabilistic temporal dependencies among different threats to identify the severity of exploiting vulnerabilities, (iii) the probability of the system as a whole going down, etc. A few examples of relevant properties on InsecureStore include:

- (1)  $P_{=?}[F \text{ systemDown}]$  to determine the probability that the system will be compromised.
- (2)  $P_{>0.8}[F$  ExploitedFrontendCWE77] to check if the probability that a the attacker will finally gain  $g_5$  by exploiting cwe77 on the Frontend component is greater than 0.8.
- (3)  $P_{=?}[\neg F \neg (QAttackBackendAuthbypass \Rightarrow F systemDown)]$ to find the probability that the system will be eventually be brought down even though the attacker gave up on gaining  $g_9$ .

## 6 SOUNDNESS

We prove that our approach is sound, i.e., the verification results of a  $\mathrm{PCTL}_{\backslash X}$  formula at the high-level model and low-level model are identical. Let  $\mathcal{D}_{\ell} = (S_{\ell}, V_{\ell}, \Sigma, s_{0\ell}, \mathbf{P}_{\ell}, \mathcal{L}_{\ell})$  be the semantics of the low-level model in terms of a DTMC, and  $\mathcal{D}_h = (S_h, V_h, \Sigma, s_{0h}, \mathbf{P}_h, \mathcal{L}_h)$  be the semantics of the high-level layer where  $V_h \subseteq V_{\ell}$ . We will prove that for all  $\mathrm{PCTL}_{\backslash X}$  formulas  $\psi$  defined over  $V_h, \mathcal{D}_{\ell} \models \psi$  if and only if  $\mathcal{D}_h \models \psi$ . To prove this, we first show that  $\mathcal{D}_{\ell}$  and  $\mathcal{D}_h$  are probabilistic weak bisimilar [3].

**Definition 6** (Probabilistic Weak Bisimulation [3]). Let  $\mathcal{Y}(s, \tau^* a \tau^*, t)$  be a function that shows the probability of reaching the state t from s using internal actions  $\tau$  and the action a. A weak probabilistic bisimulation over the DTMC  $\mathcal{D} = (S, V, \Sigma, s_0, \mathbf{P}, \mathcal{L})$  is an equivalence relation  $R \subseteq S \times S$  such that for all  $(s_1, s_2) \in R, \lambda \in \Sigma \setminus \{\tau\} \cup \{\varepsilon\}$ , and all equivalence classes  $C \in S_R, \mathcal{Y}(s_1, \tau^* \lambda \tau^*, C) = \mathcal{Y}(s_2, \tau^* \lambda \tau^*, C)$ .

Note that  $\varepsilon$  is an empty trace,  $\varepsilon \pi = \pi$ , and  $S_R$  shows the set of equivalence classes defined by R. This relation informally states that two probabilistic weak bisimilar states have the same probability to reach an equivalence class. We say two DTMCs  $\mathcal{D}_i = (S_i, V_i, \Sigma_i, s_{0i}, \mathbf{P}_i, \mathcal{L}_i), i \in \{1, 2\}$  are probabilistic weak bisimilar, denoted by  $\mathcal{D}_1 \approx_p \mathcal{D}_2$ , if and only if there exists a probabilistic weak bisimulation relation R defined over their disjoint union such that  $(s_{01}, s_{02}) \in R$ . It has been proven that two probabilistic weak bisimilar DTMCs coincide on PCTL<sub>\X</sub> [4]:

**Theorem 1.** Let  $\mathcal{D}_1$  and  $\mathcal{D}_2$  be two probabilistic weak bisimilar DTMCs. For any PCTL<sub>X</sub> formula  $\psi$ ,  $\mathcal{D}_1 \vDash \psi \Leftrightarrow \mathcal{D}_2 \vDash \psi$ .

**Lemma 1.** Let  $\mathcal{M} = \mathcal{M}_0 \parallel \cdots \parallel \mathcal{M}_n$  be a low-level model,  $\mathcal{D}_i$  be the semantics of  $\mathcal{M}_i$ ,  $0 \le i \le n$ , and  $\mathcal{D}_\ell$  be the semantics of  $\mathcal{M}$ . The fragment  $\mathcal{D}_i$  is probabilistic weak bisimilar to the low-level model  $\mathcal{D}_\ell$ , i.e.,  $\mathcal{D}_i \approx_p \mathcal{D}_\ell$ .

**Lemma 2.** The fragment  $\mathcal{D}_i = (S_i, V_i, s_{0i}, \mathbf{P}_i, \mathcal{L}_i)$  and the abstract layer  $\mathcal{D}_h = (S_h, V_h, s_{0h}, \mathbf{P}_h, \mathcal{L}_h)$  are probabilist weak bisimilar, i.e.,  $\mathcal{D}_h \approx_p \mathcal{D}_i$ .

*Proof Sketch.* To prove this theorem, we should find a relation  $R \subseteq S_i \times S_h$  that is a witnessing probabilistic weak bisimulation relation. We will show that the following relation is a witnessing weak bisimulation relation and  $(s_{0i}, s_{0h}) \in R$ :

$$R = \{ \langle s, s' \rangle \mid s =_{\{\delta_i, g_i\}} s' \}$$

where  $\delta_i$  and  $g_i$  are respectively the give up and goal capability of fragment  $\mathcal{D}_i$ . The proof is similar to that of Lemma 1, with the difference that we use the fact that the sum of the probabilities of all paths to a goal state s' where  $g_i \in \mathcal{L}_i(s')$ is equal to  $\mathsf{P}_{=?}[F g_i]$  according to the semantics of PCTL.  $\Box$ 

**Theorem 2.** The low-level model  $\mathcal{D}_{\ell}$  and the abstract layer  $\mathcal{D}_h$  are probabilistic weak bisimilar, i.e.,  $\mathcal{D}_h \approx_p \mathcal{D}_{\ell}$ .

PROOF. Since probabilistic weak bisimulation is an equivalence relation, it is also transitive. Hence, we conclude  $\mathcal{D}_h \approx_p \mathcal{D}_\ell$  from  $\mathcal{D}_h \approx_p \mathcal{D}_i$  (Lemma 2) and  $\mathcal{D}_i \approx_p \mathcal{D}_\ell$  (Lemma 1).  $\Box$ 

**Theorem 3** (Soundness). For all  $\text{PCTL}_{\setminus X}$  formulas  $\psi$ ,  $\mathcal{D}_{\ell} \vDash \psi$  iff  $\mathcal{D}_h \vDash \psi$ .

PROOF. Since  $\mathcal{D}_{\ell} \approx_p \mathcal{D}_h$  according to Theorem 2 and probabilistic weak bisimulation coincides with equivalence for  $\mathrm{PCTL}_{\backslash X}$  according to Theorem 1, we can conclude  $\mathcal{D}_{\ell} \vDash \psi$  iff  $\mathcal{D}_h \vDash \psi$  for any  $\mathrm{PCTL}_{\backslash X}$  formula  $\psi$ .

Theorem 3 proves the soundness of our approach.

## 7 EVALUATION

To study the scalability of our approach, we have conducted a set of experiments on ZNN<sup>1</sup>, a well known self-adaptive exemplar (See [52] for more experiments on an additional case study). It is an implementation of a news service implemented in a client-server architecture. ZNN's components include a set of servers managed by a load balancer that acts as a middle point to facilitate communication between the servers and clients. News are stored in a database and fetched periodically by the servers. The adaptation operations include enabling or disabling a server, and increasing or decreasing fidelity of one or multiple servers. For vulnerability scanning, we employed Progpilot $4^2$  and OpenVAS<sup>3</sup> to scan ZNN's attack surface which collectively discovered 54 vulnerabilities. ZNN is equipped with multiple adaptation strategies. We selected three among them that cover a varying number of changes to the system architecture to use in our experiments. ImproveOverallFidelity (A1) increases the system fidelity by increasing the fidelity of all servers whose fidelity is below a threshold. QuickReduceOverallCost (A2) will remove a server from the system if its cost is above a certain threshold. VariedReduceResponseTime (A3) adds a server to the server pool and then lowers fidelity to reduce response time.

Experiment Design. We consider three different types of security analysis. In the first type, called sequential analysis, there is no modularity, i.e., the analysis is performed on the low-level model. This base analysis is similar to the analysis performed in [53]. In the modular analysis case, we follow the modular model construction and analysis approach presented in Section 5. We do not, however, perform any incremental model update and verification. In the *incremental* analysis case, we perform modular model generation during the system initialization and perform incremental model update and verification upon an architectural change at runtime. We design experiments to answer the following questions:

- RQ1: How do the three analyses compare in terms of scalability and performance overhead?
- RQ2: How do the different steps of the approach compare in terms of their performance overhead?

To analyze scalability, we study how different analysis approaches perform under different initial system sizes and how they handle different numbers of architectural changes. We perform experiments with different numbers of initial components and different adaptations. We consider an initial size of 4, 6, 8, 10, 12 and 16 components. Further, we report the interesting part of our results for brevity without loss of generality, i.e., in some cases the data is partially reported in the tables. The reason for considering different adaptations is to study how the scope of changes affects performance. In the rest of this section, we will refer to a specific experiment by the number of its initial components and adaptation, for example, 12-A2 refers to the experiment in which we perform the adaptation actions associated with A2 on ZNN for 12 components. The experiments are performed on an Intel(R) Core(TM) i5-8265U CPU processor with 16GB of RAM running Debian Linux. We use the model checker PRISM version 4.7. In the following results, an experiment corresponds to one run of our runtime analysis triggered due to an adaptation. Each experiment was repeated five times <sup>4</sup>. We verify the property  $P_{=?}[F SystemDown]$  unless stated otherwise. SystemDown expands to the load balancer or any of the servers being affected by an attack.

Model Statistics. In our experiments, there are 4 to 16 components and 24 to 151 fragments. Figure 6 shows the number of states and transitions of the high-level model and the low-level model vs the number of the components of the initial architecture. The y axis uses a logarithmic scale which entails that the growth rate of both the number of states and number of transitions is almost exponential. As the number of components grows, so do the number of states and transitions of the models. Further, the low-level model failed to be built due to state space explosion in the four architectures marked with an X in Figure 6. The state size of the high-level model is smaller, as it abstracts away the internal capabilities of the low-level model. The large number of states, even for

 $<sup>^{1} \</sup>rm https://github.com/cmu-able/znn$ 

 $<sup>^{2}</sup> https://github.com/designsecurity/progpilot$ 

<sup>&</sup>lt;sup>3</sup>http://www.openvas.org/

 $<sup>^4{\</sup>rm The}$  full set of results alongside a virtual machine containing our implementation and experiments is available at https://doi.org/10. 5281/zenodo.11524518.



architectures with a small number of components, is due to the multiple vulnerabilities present in the components.

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#C	#A	#F	Security Model			Property Verifi-			Total Time (s)		
			Construction			cation (s)			.,		
			(s)								
			Seq	Mod	Inc	Seq	Mod	Inc	Seq	Mod	Inc
	A1	24	1.36	3.83	2.37	0.01	0.003	0.003	1.37	3.83	2.37
4	A2	24	0.54	3.60	0.59	0.01	0.003	0.003	0.55	3.60	0.59
-1	A3	31	0.54	4.28	3.13	0.01	0.003	0.003	0.55	4.28	3.13
	A1	104	OOM	24.52	17.26	OOM	5.93	2.14	OOM	30.45	19.40
12	A2	104	OOM	24.53	9.94	OOM	6.46	1.40	OOM	30.99	11.34
12	A3	111	OOM	27.13	17.59	OOM	5.98	6.30	OOM	33.11	23.89
	A1	144	OOM	40.07	33.61	OOM	1382.76	612.87	OOM	1422.83	646.48
16	A2	144	OOM	39.01	18.86	OOM	1401.8	551.18	OOM	1441.81	570.04
10	A3	151	OOM	38.80	33.66	OOM	1418.16	1374.43	OOM	1456.97	1408.09

Table 1: Analysis time of the different approaches

Performance Analysis. Table 1 shows the analysis time for the sequential, modular and incremental analyses with respect to the number of initial components (#C), the adaptation scenario (#A) and the number of fragments after adaptation (#F). The security model construction column shows the time required to construct the whole security model, the next column gives the time for verifying the user property and the last column shows the total time. There are multiple cases where our model checker ran out of memory, which is marked with OOM in the table. Note that the number of fragments being the same does not entail that no changes were performed or that the resulting architectures are identical.

Table 2: Security model construction time per task

Table 1. Security model combinaction time per tabl										
#C	#A	#F	Fragme	nt Graph	ıs	Fragme	nt Model	High-level		
			Generation (s)			Constru	ction (s)	Mode	l Con-	
								struction (s)		
	Seq			Mod	Inc	Seq	Mod	Inc	Mod	Inc
	A1	24	0.19	0.53	0.80	1.17	2.97	1.23	0.33	0.33
4	A2	24	0.12	0.45	0.22	0.43	2.80	0.03	0.35	0.34
4	A3	31	0.12	0.41	0.59	0.43	3.34	2.07	0.34	0.46
	A1	104	0.15	6.15	4.22	OOM	17.52	12.01	0.82	0.74
10	A2	104	0.14	3.25	3.65	OOM	20.46	5.55	0.81	0.73
12	A3	111	0.15	5.15	5.61	OOM	21.12	11.00	0.84	0.83
	A1	144	0.19	11.45	12.95	OOM	23.52	17.97	5.08	2.72
10	A2	144	0.17	10.33	8.56	OOM	24.50	6.72	5.17	3.5
10	A3	151	0.18	8.69	9.93	OOM	24.86	18.58	5.25	5.14

RQ1-1: Scalability. The first part of RQ1 which refers to the scalability of different analyses is addressed by Figure 6. Our results show that our approach can be used for small to medium-sized applications with up to a few tens of components. The results indicate that both the modular and the incremental analyses outperform the sequential one, noticeably reducing the model size to be verified, as the number of components grows from small-sized to medium-sized systems. For example, the number of states in the high-level model is reduced to  $2 \times 10^3$  from  $4.5 \times 10^8$  states in the case of 8 components. Note that the scale in Figure 6 is logarithmic, hence, the number of states and the number of transitions almost grows exponentially. In other words, our modular and incremental analyses can handle systems with a significantly larger state space compared to the sequential approach.

RQ1-2: Performance Overhead of different analyses. In terms of performance overhead of different approaches, i.e., the second part of RQ1, when the number of fragments is small, the sequential analysis outperforms the modular approach due to the overhead of building multiple fragment graphs in addition to building the high-level model by performing verification on the fragment models of the low-level model according to Table 1. The incremental analysis can be competitive in terms of performance with the sequential one when the number of changes to the system is small, as shown in 4-A2. As the number of fragments grows, the situation is reversed and the modular and incremental analyses outperform the sequential one. When the number of changes to the system is relatively small, the incremental analysis also outperforms the modular analysis. Conversely, when a large number of architectural changes occur between two analysis steps, in some cases, the bookkeeping and extra analvsis steps required for the incremental analysis incur a slight performance overhead compared to the modular analysis.

RQ2: Performance Overhead of the Analysis Steps. As shown in Table 2, for sequential analysis, the majority of the analysis time is devoted to the low-level model construction and verification. All verification is done on the low-level model, the state space of which grows exponentially. For modular analysis, when the number of fragments is small, the construction and verification of the fragment models is the major contributing factor to the analysis time. As the number of fragments grows, the construction and verification of the high-level model affects the analysis time more. Two factors contribute to this outcome: (i) the states of the high-level model also grow exponentially, while the average number of states per fragment remain rather constant and (ii) the fragment graph generation and low-level model generation and verification are performed in parallel. Conversely, the highlevel model is a single model the verification of which is not parallelized. The incremental analysis shares its performance overhead characteristics with the modular analysis, except, its low-level model construction and generation performance is improved due to the fact that it reuses past results.

Incremental vs Modular analyses. The modular analysis does not require to maintain the past runtime architectural models, in contrast to the incremental analysis that requires this model and the past analysis results. Hence, in case of small changes to the architecture, the incremental analysis will perform better than the modular one by largely reusing the old results. However, the extra bookeeping costs can incur a performance loss in case of large architectural changes.

Threats to Validity and Applicability. The system architecture, its existing vulnerabilities and the ruleset used to generate attacks and their effects impact the analysis results. Hence, alterations in the architectural style or vulnerability and ruleset density of the systems analyzed might yield different results. We believe that further experimentation with different architectures and rule inputs is required to better evaluate the effectiveness of our approach and its applicability to larger systems. The main threats to the applicability of our approach include: (i) reasoning only about known knowledge, i.e., we are unable to reason about unknown vulnerabilities or attacks, and (ii) proper setting of probabilities.

#### 8 RELATED WORK

Modular information flow control [14] approaches ensure properties independently on each fragment of the program, protocol or software system. The enforced properties are then proven to be preserved at the whole program [47], protocol [1] or system level [58]. Information flow control approaches have also been proposed for component-based system security. In [49], the authors present a model-driven security framework to design and implement secure-by-construction component-based systems. Greiner et al. [26, 27], propose an approach for modular verification using dependency clusters, i.e., equivalence relations used to compositionally verify non-interference properties system-wide in a service-based system by composing simpler non-interfering services. The above approaches design secure-by-construction systems with a static architecture; they have not been designed with dynamic software architectures or evolving systems in mind, i.e., the compositionality of services and behaviors might not be maintained once the architecture changes.

Nejati et al. [42] provide a review of approaches to tackle state space explosion in component-based system verification. In [7], the authors use a contract-based design to specify correct interactions between modules and incrementally verify changes. In [32], the authors combine compositional verification and assume-guarantee reasoning with incremental verification to analyse component-based systems whose components and structure change dynamically at runtime. The authors in [60] present a modular and incremental approach to verify that adaptive programs adhere to requirements specified in A-LTL. The authors in [50] propose an approach based on assume-guarantee reasoning to verify adaptive embedded systems under environment constraints. The author in [33] proposes a modular symbolic method to synthesize a controller to support adaptations under partial observations and enforce safety properties. The above approaches aim for verification of functional or safety properties and do not concern themselves with security. Our work focuses on verification of probabilistic behaviors and is able to model and analyze the security of a dynamic systems at runtime.

In the context of component-based systems [11], runtime verification has been employed to check whether a componentbased system adheres to its specification at runtime [19, 21]. In dynamically reconfigurable architectures and self-adaptive

systems [15, 40, 55] verification has been employed to check whether reconfigurations or adaptations of the system architecture adhere to functional constraints. Filieri et al. [23] discuss and compare two parametric model checking approaches [13, 22] to improve the performance of verifying (R-)PTCL properties on DTMCs at runtime in self-adaptive systems. Parametric approaches require that the PCTL properties are known at design time so that precomputation is possible. In our approach, the security properties need not be known at design time and can be adjusted dynamically as the system evolves to accommodate new security requirements that might arise due to evolution. The above approaches aim to either check or ensure functional correctness and do not consider security. In contract to this paper, they verify the system behavior against given properties while we perform threat analysis by analyzing the attacker's behavior.

Runtime security analysis techniques often utilize runtime monitoring [20], to check if a specification is satisfied by the current execution or not [16, 31, 59], while we focus on threat analysis to compute security risks. Since these approaches check only one execution, their analysis results are incomplete. We perform full analysis at runtime, and consider all possible executions, which potentially leads to a higher performance overhead. Khakpour et al. [35] present a method to perform quantitative security risk assessment on adaptations considering dependencies of components vulnerabilities, which is informal in contrast to our approach. Skandylas et.al [53], model check security threats of a self-adaptive system during adaptation using UPPAAL [8]. The main goal of [53] was to analyze the security of all adaptation options in the system and select the most secure adaptation to reduce the exposed adaptation surface. They perform the verification on a model that corresponds to the low-level layer of our security model. However, this approach suffers from scalability issues.

#### 9 CONCLUSION

In this paper, we proposed an automated approach for scalable security analysis of component-based systems with dynamic architectures. We exploited the power of formal abstraction and modular verification to build a two-tier probabilistic security model, which is maintained at runtime and incrementally updated. Security properties are then verified against the top abstract model for threat analysis. As future work, we plan to incorporate attack mitigation techniques into our approach by designing countermeasures that can be evaluated and applied at runtime. We also aim to investigate other techniques, e.g., assume-guarantee reasoning [37], hierarchical modeling and analysis [30] or improving our attack partitioning algorithm, to further improve the scalability of our approach to analyze larger systems.

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