MASACIFA Faster and Secure Memory Nanagement



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1





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Memory related bugs are serious issues



Momory salety

browser are memory safety issues

Not memory safety

~70% of the vulnerabilities found by Microsoft and in Google Chrome web

https://www.chromium.org/Home/chromium-security/memory-safety





Memory allocators must also be fast

Hundreds of millions of malloc calls per second in typical data-intensive workloads

binary tree benchmark on M1 macbook

status, 4.9 billions of malloc calls in 7:9 seconds for a tm_send_last, fmt: "%u %u", nparams: 2): end_all", tm_send_all, fmt "%u %u", nparams: 2); me: "tm_send_from", tm_send_from, fmt "%u %u %u", nparams: 3); e: "tm_set_ack", tm_set_ack, fmt "%u %u", nparams: 2); e: "tm_send_cmds", tm_send_cmds, fmt: "gam Hifdof 11



Customised memory allocators for hardware and applications



CHERI Capabilities



Arm memory tag extension







Intel Software Guard Extensions



Oracle Silicon Secured Memory





But memory allocators are expensive to build

e.g. Jemalloc would cost £2.1m to develop

Estimate based David A. Wheeler's SLOCCount estimation tool: https://dwheeler.com/sloccount/ and a typical UK software engineer salary by Glassdoor











M4Secure – making it easier to develop fast, correct and secure memory management libraries



Basic memory allocator code Machine learning for code synthesis and optimisation

Hardware and software security properties and rules



Improved code to use hardware security features

Model checker for verification

Feedback







LLVM Compiler

Leeds is developing a super optimiser with mutation rules designed for Cheri (ARM Morello) and LLVM IR



Super optimisation examples

%0:i8 = var %1:i8 = lshr %0, 3:i8 %2:i1 = eq %1, 0:i8 infer %2



%3:i1 = ult %0, 8:i8 result %3

mov \$8, %eax sub %ecx, %eax dec %eax



mov \$7, %eax sub %ecx, %eax

constant folding

sub %eax, %ecx test %ecx, %ecx je .END mov %edx, %ebx



sub %eax, %ecx cmovne %edx, %ebx

Branch elimination

Super optimisation for LLaMA large language model



Cheri Morello

5% performance improvement without changing the user code!



Reinforcement learning (RL) to drive super



strategies

Security properties for memory allocators

• e.g. Linear Temporal Logic (LTL) property:

$\Box((\diamond q) \rightarrow ((!qUp)\&\&(!rUq)\&\&(\diamond r)))$

- occur eventually, but not before q "

• Where q is client access mem, p is malloc and r is free. • "if in the future q will happen, then p must occur before q, and r must

Model checking to verify security properties

- Develop domain specific language to generate models on the fly
- Labels in code to mark the functionalities
 - E.g. mem_allocate_start/end to trigger relevant flags
- Currently use SPIN or stripped-down version of nested depth-first search for LTL model checking

 and support realtime verification of safety properties

Cheri benchmarks for memory allocators

- 10 allocation-intensive C benchmarks
- Regular and irregular allocation patterns
- https://github.com/glasgowPLI/allocbench



130	<pre>struct batch* dequeue_batch() {</pre>
131	<pre>pthread_mutex_lock(&lock);</pre>
132	<pre>while (batches == NULL && !atomic_load(&done_flag)) {</pre>
133	<pre>pthread_cond_wait(∅_cv, &lock);</pre>
134	}
135	<pre>struct batch* result = batches;</pre>
136	<pre>if (result) {</pre>
137	<pre>batches = result->next_batch;</pre>
138	<pre>batch_count;</pre>
139	<pre>pthread_cond_signal(&full_cv);</pre>
140	}
141	<pre>pthread_mutex_unlock(&lock);</pre>
142	return result;
143	}
144	
145	<pre>void *mem_allocator (void *arg) {</pre>
146	<pre>int thread_id = *(int *)arg;</pre>
147	<pre>struct lran2_st lr;</pre>
148	<pre>lran2_init(&lr, thread_id);</pre>
149	
150	<pre>while (!atomic_load(&done_flag)) {</pre>
151	<pre>struct batch *b = xmalloc(sizeof(*b));</pre>
152	<pre>for (int i = 0; i < OBJECTS_PER_BATCH; i++) {</pre>
153	<pre>size_t siz = object_size > 0 ? object_size : possible_sizes[lran2(&lr</pre>
154	<pre>b->objects[i] = xmalloc(siz);</pre>
155	<pre>memset(b->objects[i],i%256,(siz > 128 ? 128 : siz));</pre>
156	}
157	<pre>enqueue_batch(b);</pre>
158	}
159	return NULL;
160	}
4.64	



Conclusions

- It is high time to make memory management secure while high-performant and correct correct memory allocators Should generalise beyond CHERI
- •ML + Formal Verification for fast, secure, and Lots of opportunities for collaboration

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 thread.meter.betch();
 threa) thruct batch* result - batches; (f (result) (batches - result-nest_batch; batch_count.-: pthrust_count.-: 1 aid "magallocator (vaid "arg) int Wrand_id = "Ent "Jergs struct Energist Bry EnergisticKErs Wrand_id(s)